

ABSTRACT OF THE DISCLOSURE

Where instruction data to enable a stop control is stored in an operation setting register that is provided in a peripheral module in an evaluation chip, a stop control circuit of the peripheral module stops the progress of an operation of a functional circuit until a CPU completes processing of a monitor program that is performed in response to a break interrupt request. None of the operation setting register, the stop control circuit, and an interface circuit are reset by a reset signal supplied from a circuit board as a target system. The CPU directly starts execution of a user program without starting the monitor program.